

IC1 4017

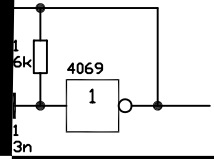
Reset

Block

● 0Z

00 01 02 03 04 05 06 07 08 09

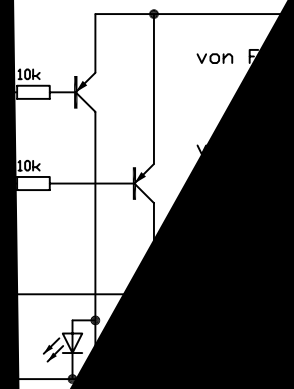
00 01 02 03



6 7 8 9

2 13

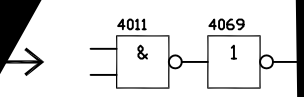
vo
vo



10k
C1815
120k
N4148

ters und des ODER
on 2 ICs auf 1 IC.

1. vert
er" d



4011
&
4011
&